

# “Quads” mask set

New mask set for providing the UK with quad tiles for proving modules. It will also provide some single chips for R&D purposes (edge reduction, irradiation studies). Due to present uncertainties for the geometry details of the future FE-I4 (and the cutting of some redundant circuitry at the edge of the chip) our design has to accommodate for it. The design will include 4 quads with large gaps (conservative, works with present chip as is, needs to have rather large pixels at the border with two ASICs): 3 of these have 450 $\mu$ m distance (D) from edge, 1 will have D=350 $\mu$ m. 1 quad will have the best guess for the smaller gap (shortest pixels at ASICs boarder) with D= 450 $\mu$ m. There are 8 SC.

